

For immediate release

3rd October 2011

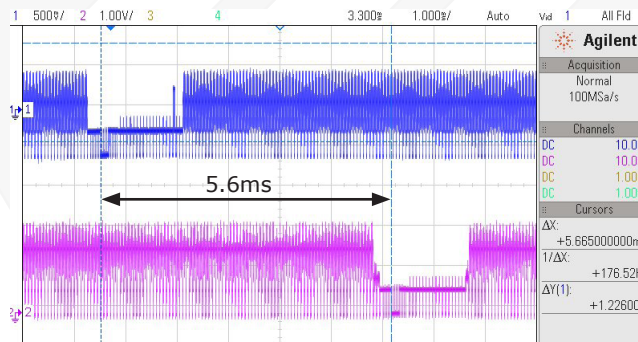
Announcement

Non-visible Latency For Wireless Video Links With FPGA-based Encoder & Decoder System

Potsdam, Germany - On 3rd October ITAVA Systems announces the advancement of its unique Live CoDec ULD product line.

ITAVA Systems again accomplished pioneer work with a remarkable enhancement of the speed and quality of the world's lowest end-to-end latency encoding-decoding-system. With a delay of below 6ms it is outstanding in the world of wireless video transmission. The CoDec IP-Cores are customizable on request, will fit all major interface requirements and are currently implemented for SD resolution using bandwidths below 10MBit/s.

Reaching an end-to-end latency below 10ms using a common wireless link, the FPGA-based IP-Core enables live-control and live-perception of sophisticated remote control applications.



Measurement of delay between video input (top) and decoding output (bottom) using ASI serial link = 5.6ms

About

ITAVA Systems is a world leader in the development of low latency video coding and embedded system design. The company combines almost 15 years of experience in the embedded systems area especially in FPGA programming as well as image processing and video coding. This experience enables ITAVA Systems to implement customer-specific solutions in programmable logic or provide netlists for ASIC tapeouts.

An ASI-connected FPGA demonstrator system is available on request. For more specific information visit www.itava.net or contact mail@itava.net.